

FIG. 1  
(PRIOR ART)

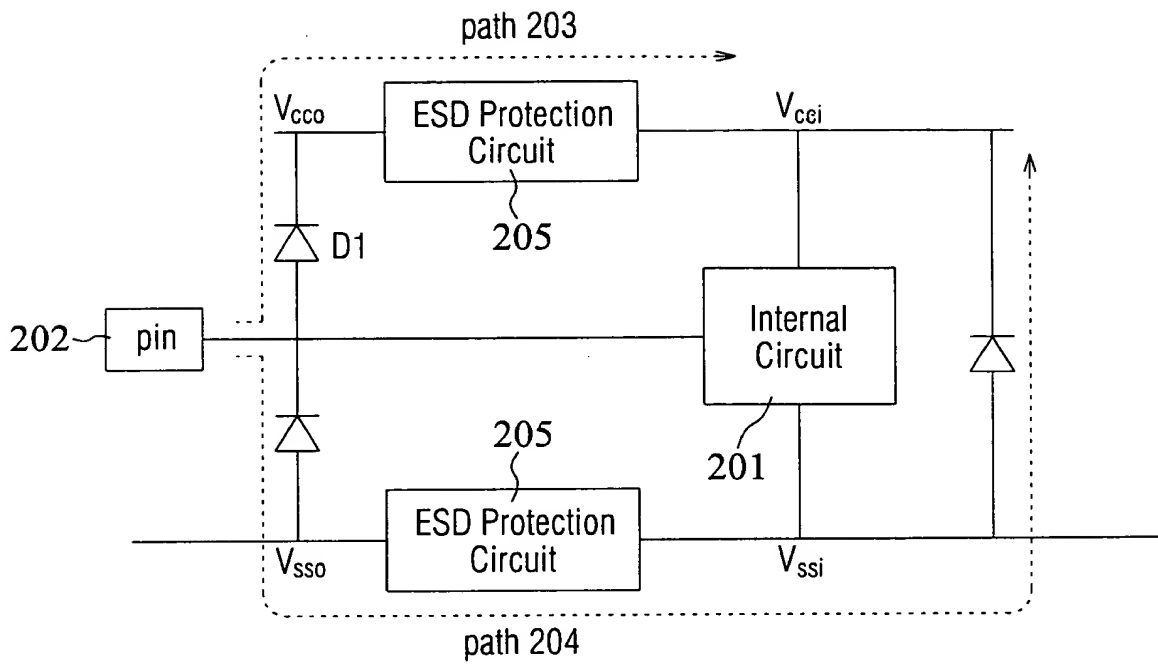


FIG. 2  
(PRIOR ART)

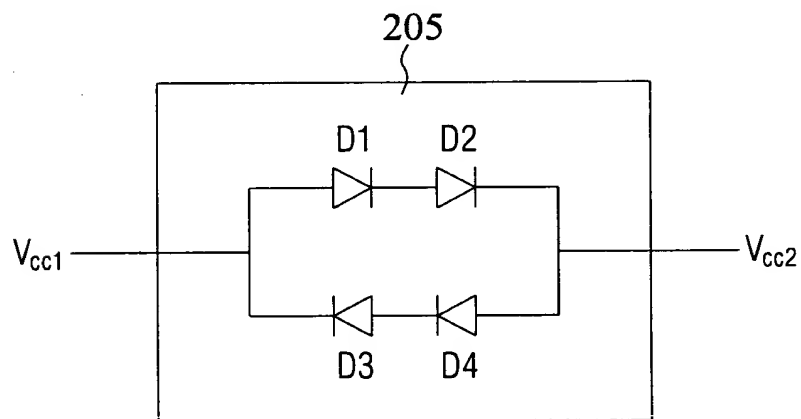


FIG. 3  
(PRIOR ART)

0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99

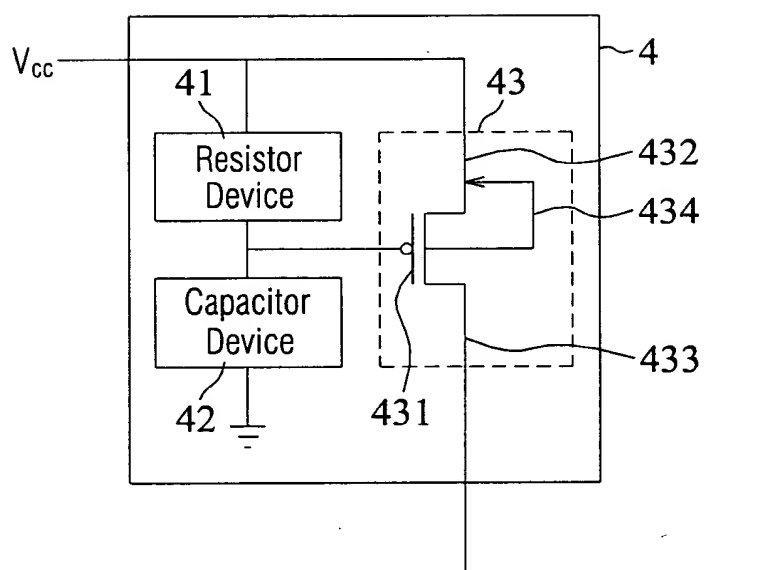


FIG. 4

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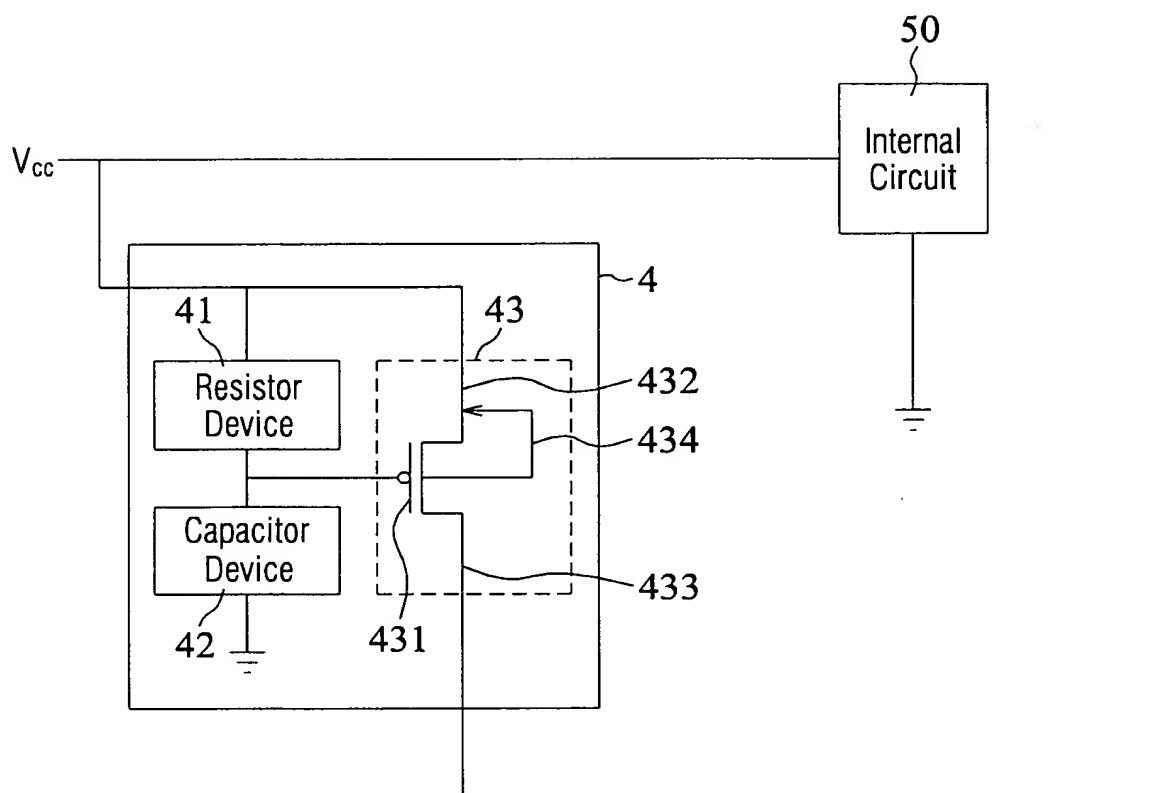


FIG. 5

Figure 1 shows two circuit blocks, 61 and 62, connected to a common ground 63. Block 61 is connected to  $V_{cc1}$  and contains a Resistor Device (611), a Capacitor Device (612), and a switch (613) with sub-components 6131, 6132, and 6134. Block 62 is connected to  $V_{cc2}$  and contains a Resistor Device (621), a Capacitor Device (622), and a switch (623) with sub-components 6231, 6232, and 6234, plus a diode (71).

FIG. 6

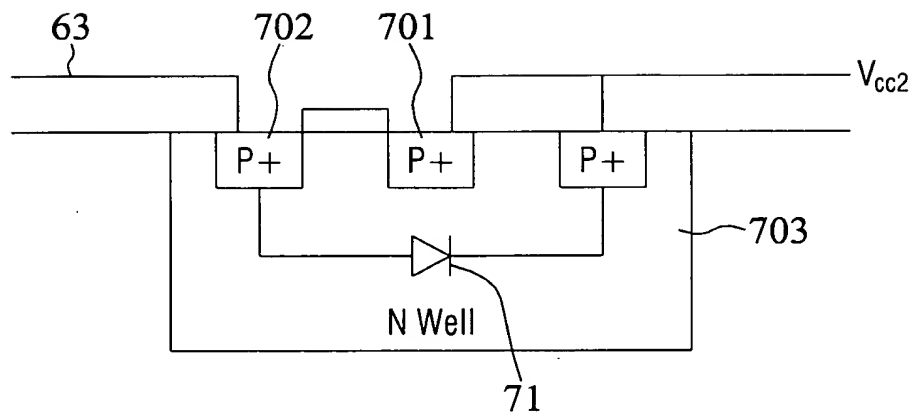


FIG. 7